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Class Subclass

ISSUE CLASSIFICATION

SCANNED

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| UTILITY SERIAL NUMBER | PATENT DATE | PATENT NUMBER |
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| SERIAL NUMBER | FILING DATE | CLASS | SUBCLASS | GROUP ART UNIT | EXAMINER |
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APPLICANTS

CPA

CONTINUING DATA***
VERIFIED THIS APPLN IS A DIV OF 08/650,719 05/20/96
yes lth

FOREIGN APPLICATIONS***
VERIFIED
None lth

Best Available Copy

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|---|--|-------------|---------------------|------------------|-----------------|------------------|------------------------|--------------------------|
| Foreign priority claimed 35 USC 119 conditions met | <input type="checkbox"/> yes <input checked="" type="checkbox"/> no | AS FILED | STATE OR COUNTRY | SHEETS DRWGS. | TOTAL CLAIMS | INDEP. CLAIMS | FILING FEE RECEIVED | ATTORNEY'S DOCKET NO. |
|---|--|-------------|---------------------|------------------|-----------------|------------------|------------------------|--------------------------|

Verifed and Acknowledged lth Examiner's Initials

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TITLE

BURST/PIPELINED EDO MEMORY DEVICE

Memory Device with Mode Selection Circuitry for
Burst or Pipelined Operation

asynchronously - Accessible

U.S. DEPT. OF COMM./PAT. & TM - PTO-436L (Rev. 12-94)

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|--|-----------|--------------------------|-------------|
| PARTS OF APPLICATION FILED SEPARATELY | | Applications Examiner | |
| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
| | | Total Claims | Print Claim |
| Assistant Examiner | | | |
| ISSUE FEE | | DRAWING | |
| Amount Due | Date Paid | Sheets Drwg. | Figs. Drwg. |
| | | Print Fig. | |
| Label Area | | ISSUE BATCH NUMBER | |
| Primary Examiner | | | |
| PREPARED FOR ISSUE | | | |
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Form PTO-436A
(Rev. 8/92)

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